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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kim, et al.

Serial No: 09/574,653

Examiner: Hsien Ming Lee

Filed: 05/18/2000

For: A TUNABLE SIDEWALL SPACER PROCESS FOR CMOS INTEGRATED CIRCUITS

Docket No: TI-29012

Conf. No: 8503

Art Unit: 2823

#8/a
8/28/2
Lunles

AMENDMENT UNDER 37 C.F.R. § 1.111

Assistant Commissioner For Patents
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 8-6-02.

Ann Trent
Ann Trent

Dear Sir:

Responsive to the Office Action mailed April 1, 2002, in connection with the above identified application, Applicants respectfully submit the following amendments and remarks.

RECEIVED
AUG 22 2002
TECHNOLOGY CENTER 2800